



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

KIMURA et al.

Atty. Ref.: 1035-473; Confirmation No. 4031

Appl. No. 10/668,166

TC/A.U. 2811

Filed: September 24, 2003

Examiner: ARENA, Andrew O.

For: SEMICONDUCTOR DEVICE AND CHIP-STACK SEMICONDUCTOR DEVICE

\* \* \* \* \*

November 1, 2007

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**AMENDMENT**

Responsive to the Official Action dated July 2, 2007 (for which petition is hereby made for a one month extension of time), please amend the above-identified application as follows:

11/02/2007 MBELETE1 00000050 10668166

01 FC:1251

120.00 0P